	Application No.	Applicant(s)
Notice of Allowability	10/623,474	SCHILLACI ET AL.
	Examiner	Art Unit
	Tuan V. Thai	2186
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this appropriate communication (GHTS). This application is subject	oplication. If not included on will be mailed in due course. THIS
1. \boxtimes This communication is responsive to <u>amendment filed 11/3</u>	<u>80/2006</u> .	
2. X The allowed claim(s) is/are 2-6, 8-13, 17-20 renumbered a.	s 2,1,3-15 respectively.	
 3. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give 1. Substitute of Draftspers	e been received. e been received in Application No cuments have been received in this of this communication to file a reply lENT of this application. itted. Note the attached EXAMINER es reason(s) why the oath or declar on's Patent Drawing Review (PTC as Amendment / Comment or in the least to should be written on the draw he header according to 37 CFR 1.121 sit of BIOLOGICAL MATERIAL	complying with the requirements R'S AMENDMENT or NOTICE OF ation is deficient. 0-948) attached Office action of ings in the front (not the back) of (d). must be submitted. Note the
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal 6. Interview Summan Paper No./Mail Da 7. Examiner's Amend 8. Examiner's Statem 9. Other	y (PTO-413), ate

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Attorney's Docket No.: 856063.743

IN THE UNITED STATES PATENT AND

TRADEMARK OFFICE

In re application of: Schillaci et al. Group: 2186

Serial No.: 10/623,474 Examiner: Tuan Thai

For: AUTOMATIC DECODING METHOD FOR MAPPING AND SELECTING A

NON-VOLATILE MEMORY DEVICE HAVING A LPC SERIAL

COMMUNICATION INTERFACE IN THE AVAILABLE ADDRESSING

AREA ON MOTHERBOARDS.

1. This action is responsive to amendment filed November 30, 2006. Claims 1, 7 and 14-16 have been cancelled. Claims 2-6, 8-13 and 17-20 are now allowed.

REASONS FOR ALLOWANCE

2. The following is an Examiner's Statement of Reasons for Allowance:

The prior arts of record do not teach or suggest, alone or in combination, **all** the limitations of the independent claims of the current invention (claims 3, 8, 11, 17 and 18). Claims 3, 11 and 17 references a "method for mapping and selecting a non-volatile memory device having a LPC serial interface"; claims 8

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and 18 references "a non-volatile memory device equipped with an interface with LPC serial protocol and a plurality of address The discussion of the reasons for allowance shall be directed to claim 3 in which the Examiner shall designate as the primary invention in this application; however, the reasons for allowance will also apply to all other indicated independent claims (claims 8, 11, 17 and 18). The prior arts of record do not teach nor suggest a system and method for mapping and selecting a non-volatile memory device having a LPC serial communication interface, wherein the memory is equipped with a plurality of addressing pins and mounted on a motherboard together with other memories of the same type bi-directionally connected with a controller putting it into communication with a processor housed in turn on the motherboard comprises a processor that compares the addressing pins of each memory with a portion of the addressing coding bits both to identify the addressing type to be used, top-down or bottom-up, and to determine which memory is polled by the controller for a given operation, wherein the addressing coding is a thirty-two-bit coding and the most significant bits A<31 :25> are used to identify the addressing type, while some intermediate bits A<24:21> are used for being compared with the addressing pins to determine which memory is polled by the controller. In light of the foregoing, claims 3, 8

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11 and 17-18 of the present application are found to be patentable over the prior arts.

Claims 2, 4-6, 9-10, 12-13 and 19-20 further limit the allowable independent claims 3, 8, 11 and 17-18. These claims are therefore allowable for the same reason as set forth above.

Any comments considered necessary by Applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan V. Thai whose telephone number is (571)-272-4187. The examiner can normally be reached on from 6:30 A.M. to 4:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew M. Kim can be reached on (571)-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through

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TVT/January 17, 2007

Tuan V. Thai

PRIMARY EXAMINER

Group 2100